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I. M. Elfadel, David D. Ling

June 1997 **Proceedings of the 34th annual conference on Design automation DAC '97**

Publisher: ACM Press

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CAD tools and research in the area of reduced-order modeling of large linear interconnect networks have evolved from merely finding a Padé approximation for the given network transfer function to finding an approximate transfer function that preserves such circuit-theoretic properties of the network as stability, passivity, and RLC synthesizability. In particular, preserving passivity guarantees that the reduced-order models will be well-behaved when embedded back in the circuit where the interconnect ...

2 [Gradient-based optimization of custom circuits using a static-timing formulation](#)



A. R. Conn, I. M. Elfadel, W. W. Molzen, P. R. O'Brien, P. N. Strenski, C. Visweswariah, C. B. Whan

June 1999 **Proceedings of the 36th ACM/IEEE conference on Design automation**

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3 [A block rational Arnoldi algorithm for multipoint passive model-order reduction of multiport RLC networks](#)

I. M. Elfadel, David D. Ling

November 1997 **Proceedings of the 1997 IEEE/ACM international conference on Computer-aided design**

Publisher: IEEE Computer Society

Full text available: pdf (225.42 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)[Publisher Site](#)

Recent work in the area of model-order reduction for RLC interconnect networks has been focused on building reduced-order models that preserve the circuit-theoretic properties of the network, such as stability, passivity, and synthesizability. Passivity is the one circuit-theoretic property that is vital for the successful simulation of a large circuit netlist containing reduced-order models of its interconnect networks. Non-passive reduced-order

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